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06/30/2004

BIRCH, STEWART, KOLASCH & BIRCH, LLP
 PO BOX 747
 FALLS CHURCH, VA 22040-0747

10/01/2004 SSITHB2 00000006 10600466

01 FC:1501

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,466	06/23/2003	Brian S. Lee	0941-0754P	7276

TITLE OF INVENTION: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE WITH LOOP LINE PATTERN STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630 1330	09/30/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
OWENS, DOUGLAS W	2811	438-284000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Birch, Stewart,
 1 Kolasch & Birch, LLP
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

ProMOS Technologies Inc.

Hsinchu, Taiwan, R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Issue Fee
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Joe McKinney Muncy, #32,334

09/29/2004

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